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UPFC with Matrix Converter using Direct Power **Control**

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Abstract- In this paper presents a direct power control (DPC) for three-phase matrix converters operating as unified power flow controllers (UPFCs). Matrix converters (MCs) allow the direct ac/ac power conversion without dc energy storage links; therefore, the MC-based UPFC (MC-UPFC) has reduced size and cost, reduced capacitor power losses, together with higher reliability. Theoretical principles of direct power control (DPC) based on sliding mode control methods are established for an MC-UPFC dynamic model including the input filter. As a result, line active and reactive power, together with ac supply reactive power, can be directly by an appropriate matrix converter switching state assuring good steady-state and dynamic responses. Experimental results of DPC controllers for MC-UPFC show decoupled active and reactive power control, zero steady-state tracking error, and fast response times. Compared to an MC-UPFC using active and reactive power linear controllers based on a modified Venturini high-frequency PWM modulator, the experimental results of the advanced DPC-MC guarantee faster responses without overshoot and no steady-state error, presenting no cross-coupling in dynamic and steady-state responses.

Keywords: Ac/ac conversion advanced direct power controller, matrix converter, unified power flow controller

1.INTRODUCTION

Electric power flow through an alternating current transmission line is a function of the line impedance, the magnitudes of the sending-end and receiving-end voltages, and the phase angle between these voltages. Essentially, the power flow is dependent on the voltage across the line impedance. Electricity market deregulation, together with growing economic, environmental, and social concerns, has increased the difficulty to burn fossil fuels, and to obtain new licenses to build transmission lines (rights-of-way) and highpower facilities. This situation started the growth of decentralized electricity generation (using renewable energy resources). Unified power-flow controllers (UPFC) enable the operation of power transmission networks near their maximum ratings, by enforcing power flow through well-defined lines [2]-[4]. These days, UPFCs are one of the most versatile powerful flexible ac transmission systems (FACTS) devices.

The existence of a dc capacitor bank originates additional losses, decreases the converter lifetime, and increases its

weight, cost, and volume. In the last few decades, an increasing interest in new converter types, capable of performing the same functions but with reduced storage needs, has arisen [10]-[12]. These converters are capable of performing the same ac/ac conversion, allowing bidirectional power flow, guaranteeing near sinusoidal input and output currents, voltages with variable amplitude, and adjustable power factor [13]-[14]. These minimum energy storage ac/ac converters have the capability to allow independent reactive control on the UPFC shunt and series sides, guaranteeing that the active power exchanged on the UPFC series connection is always supplied/absorbed by the shunt connection. Conventional UPFC controllers do not guarantee robustness [6]-[8] and [11], [12]. In [10], the dependence of the matrix converter output voltage on the modulation coefficient was investigated, concluding that MC-UPFC is able to control the full range of power flow. Recent nonlinear approaches [5] enabled better tuning of PI controller parameters. Still, there is room to further improve the dynamic response of UPFCs, using nonlinear robust controllers.

In the last few years, direct power control techniques have been used in many power applications, due to their simplicity and good performance [12]. In this paper, a matrix converter based UPFC is proposed, using a direct power control approach (DPC-MC) based on an MC-UPFC dynamic model(Section II). In order to design UPFCs, presenting robust behaviour to parameter variations and to disturbances, the proposed DPC-MC control method, in Section III, is based on sliding mode-control techniques [9], allowing the real-time selection of adequate matrix vectors to control input and output electrical power. Sliding mode-based DPC-MC controllers can guarantee zero steady-state errors and no overshoots, good tracking performance, and fast dynamic responses, while being simpler implement requiring to and less processing power, when compared to proportional integral (PI) linear controllers obtained from linear active and reactive power models of UPFC using a modified Venturing high-frequency **PWM** modulator. The dynamic and steady-state behaviour of the proposed DPC-MC P, Q control method is evaluated and discussed detailed simulations and experimental implementation (Sections IV and V). Simulation and experimental results obtained with the nonlinear DPC for

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matrix converter-based UPFC technology show decoupled series active and shunt/series reactive power control, zero steady-state error tracking, and fast response times, presenting faultless dynamic and steady-state responses.

UPFC POWER SYSTEM

A simplified power transmission network using the proposed matrix converter UPFC is presented in Fig. 1, Fig. 2 shows the simplified three-phase equivalent circuit of the matrix UPFC transmission system model. For system modelling, the power sources and the coupling transformers are all considered ideal. Also, the matrix converter is considered ideal and represented as a controllable voltage source, with amplitude and phase ρ . In the equivalent circuit, V_{ro} is the load bus voltage; The DPC-MC controller will treat the simplified elements as disturbances.

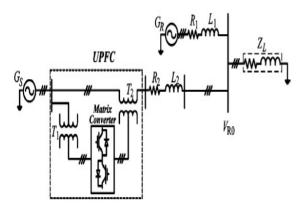


Fig 1: Transmission network with matrix converter UPFC.

Where V_s and V_r are, respectively, the sending-end and receiving-end sinusoidal voltages of the G_s $\,$ and $\,$ G_r generators feeding $\,$ load Z_l . The matrix $\,$ converter $\,$ is $\,$ connected $\,$ to transmission line 2, represented as a series inductance with series resistance (L₂ and R₂), through coupling transformers.

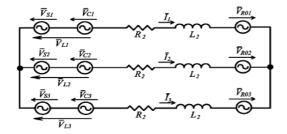


Fig 2: Three-phase equivalent circuit of the matrix UPFC and transmission line.

Considering a symmetrical and balanced three-phase system and applying Kirchhoff laws to the three-phase equivalent circuit (Fig. 2), the ac line currents are obtained in dq coordinates.

$$\frac{dI_d}{dt} = \omega I_q - \frac{R_2}{L_2} I_d + \frac{1}{L_2} (V_{Ld} - V_{R0d})$$

$$\frac{dI_q}{dt} = -\omega I_d - \frac{R_2}{L_2} I_q + \frac{1}{L_2} (V_{Lq} - V_{R0q}).$$
(2)

$$\frac{dI_q}{dt} = -\omega I_d - \frac{R_2}{L_2} I_q + \frac{1}{L_2} (V_{Lq} - V_{R0q}). \tag{2}$$

The active and reactive power of sending end generator [9] are given in dq coordinates by

$$\begin{bmatrix} P \\ Q \end{bmatrix} = \begin{bmatrix} V_d & V_q \\ V_q & -V_d \end{bmatrix} \begin{bmatrix} I_d \\ I_q \end{bmatrix}.$$

Assuming v_{R0d} and $V_{sd} = V_d$ as constants and a rotating reference frame synchronized to the source V_s so that $V_{sq} = 0$, active and reactive power P and Q are given by (4) and (5), respectively

Based on the desired active and reactive power (P_{ref} , Q_{ref}), reference currents (I_{dref} and I_{qref}) can be calculated from (4) and (5) for current controllers [20]. However, allowing P, Q actual powers are sensitive to errors in the V_d , V_q values.

2.1 Matrix Converter Output Voltages and Input **Currents**

A diagram of the UPFC system (Fig. 3) includes the three- phase shunt input transformer (with windings Ta, T_b, T_c), the three-phase series output transformer (with windings TA, TB, TC) and the three-phase matrix converter, represented as an array of nine bidirectional switches Ski with turn-on and turn- off capability, allowing the connection of each one of three output phases directly to any one of the three input phases. The three-phase (ICr) input filter is required to reestablish a voltage-source boundary to the matrix converter, enabling smooth input currents.

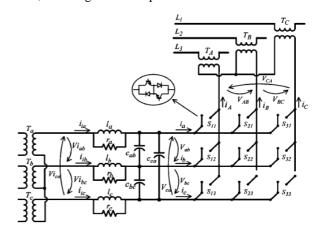


Fig 3: Transmission network with matrix converter UPFC.

Applying dq coordinates to the input filter state variables presented in Fig. 3 and neglecting the effects of the damping resistors, the following equations are obtained:

$$\begin{cases} \frac{di_{id}}{dt} = \omega i_{iq} - \frac{1}{2l}V_d - \frac{1}{2\sqrt{3l}}V_q + \frac{1}{l}V_{id} \\ \frac{di_{iq}}{dt} = -\omega i_{id} + \frac{1}{2\sqrt{3l}}V_d - \frac{1}{2l}V_q + \frac{1}{l}V_{iq} \\ \frac{dV_d}{dt} = \omega V_q - \frac{1}{2\sqrt{3}C}i_{iq} + \frac{1}{2C}i_{id} - \frac{1}{2C}i_d + \frac{1}{2\sqrt{3}C}i_q \\ \frac{dV_q}{dt} = -\omega V_d + \frac{1}{2\sqrt{3}C}i_{id} + \frac{1}{2C}i_{iq} - \frac{1}{2\sqrt{3}C}i_d - \frac{1}{2C}i_q \end{cases}$$
(6)

Where Vid, Viq, Iid, Iiq represents, input voltages and input current in dq components (at the shunt transformer secondary) and V_d , V_q , I_d , I_q , are matrix converter voltages and input currents in dq components respectively.

3. DIRECT POWER CONTROL OF MC-UPFC

3.1 Direct Control of Matrix Converters Input **Reactive Power**

In addition, the matrix converter UPFC can be controlled to ensure a minimum or a certain desired reactive power at

matrix converter input. Similar to the previous considerations.

since the voltage source input filter (Fig. 3) dynamics (6)

strong relative degree of two [25], then a suitable sliding surface S_{OI}(e_{OI}, t) (19) will be a linear combination of the desired reactive power error e_{QI=} Q_{iref} - Q_i firstorder time derivative.

$$S_{Q_i}(e_{Q_i},t) = \left(Q_{i_{\mathrm{ref}}} - Q_i\right) + K_{Q_i}\frac{d}{\mathrm{dt}}\left(Q_{i_{\mathrm{ref}}} - Q_i\right).$$

The time derivative can be approximated by a discrete time

difference, as K_{Qi} has been chosen to obtain a suitable switching frequency, since as stated before, this sliding surface needs to be quantized only in two levels (-1 and +1)

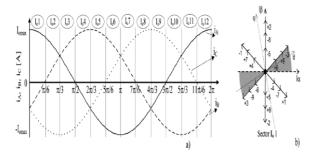


Fig 4: (a) Output currents and their corresponding sector. (b) Input current state-space vectors, when output currents are located at sector I₀₁. The axis is represented, considering that the input voltages are located in zone V₀₁.

To full fill a stability condition similar to (15), considering the input filter dynamics (6), (20) is obtained.

$$\overset{\bullet}{S}_{Qi}(e_{Qi}, t) = V_{id} \left(\frac{\operatorname{di}_{iq}}{\operatorname{dt}} + K_{Qi} \frac{\operatorname{d}^{2} i_{iq}}{\operatorname{dt}^{2}} \right) = V_{id} \left(-\omega i_{id} + \frac{1}{2\sqrt{3}l} V_{d} - \frac{1}{2l} V_{q} \right) + V_{id} K_{Qi} \left(-\omega^{2} i_{iq} + \frac{\omega}{l} V_{d} + \frac{\omega}{\sqrt{3}l} V_{q} - \frac{\omega}{l} V_{id} - \frac{i_{iq}}{3lC} + \frac{i_{q}}{3lC} \right). \tag{20}$$

From (20), it is seen that the control input, the Iq matrix input current, must have enough amplitude to impose the sign of $S_{QI}(e_{QI},$ Supposing that there is enough Iq amplitude, (9) and (12) are used to establish the criteria (13) to choose the adequate matrix input current vector that imposes the needed sign of the matrix input-phase current Iq related to the output-phase currents by (9).

 $1.IfS_{QI}(e_{QI}, t) > 0 => S_{QI}(e_{QI}, t) < 0$, then select vector with current I_q<0 to increase Qi

 $2.IfS_{QI}(e_{QI}, t) < 0 \Rightarrow S_{QI}(e_{QI}, t) > 0$, then select vector with current I_q> 0 to increase Q_i

The sliding mode is reached when vectors applied to the converter have the necessary I_a current amplitude to satisfy stability conditions, such as (15). Therefore, to choose the most adequate vector in the chosen dq reference frame, it is necessary to know the output currents location since the I_q input current depends on the output currents (Table I). Considering that the dq -axis location is synchronous with the Via input voltage (i.e., reference frame depends on the Via input voltage location), the sign of the matrix reactive power Qi can be determined by knowing the location of the input voltages and the location of the output currents (Fig.4).

Table 1: State-space vectors selection, for input voltages located at sector vil

Cα	C_{β}	Sector											
		I_012 ; I_01		I_02 ; I_03		I_04 ; I_05 C_{Qi}		$I_{\theta}6$; $I_{\theta}7$ C_{Qi}		$I_{\theta}8$; $I_{\theta}9$ C_{Qi}		$I_{\theta}1\theta;I_{\theta}11$ C_{Qi}	
		C_{Qi}		C_{Qi}									
		+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1
-1	+1	-9	+7	-9	+7	-9	+7	+7	-9	+7	-9	+7	-9
-1	0	+3	-1	+3	-1	-1	+3	-1	+3	-1	+3	+3	-1
-1	-1	-6	+4	+4	-6	+4	-6	+4	-6	-6	+4	-6	+4
0	+1	-9	+7	-9	+7	-9	+7	+7	-9	+7	<u>-9</u>	+7	-9
0	0	-2	+2	+8	-8	-5	+5	+2	-2	-8	+8	+5	-5
0	-1	-7	+9	-7	+9	-7	+9	+9	-7	+9	-7	+9	-7
+1	+1	-4	+6	+6	-4	+6	-4	+6	-4	-4	+6	-4	+6
+1	0	+1	-3	+1	-3	-3	+1	-3	+1	-3	+1	+1	-3
+1	-1	-7	+9	-7	+9	-7	+9	+9	-7	+9	-7	+9	-7

4.SIMULATION RESULTS

The performance of the proposed direct control system was evaluated with a detailed simulation model using MATLAB/SIMULINK the **SIMPOWERSYSTEMS** to represent the matrix converter, transformers, sources and transmission lines, and SIMULINK blocks to simulate the control

system. Ideal switches were considered to simulate matrix converter semiconductors minimizing simulation times

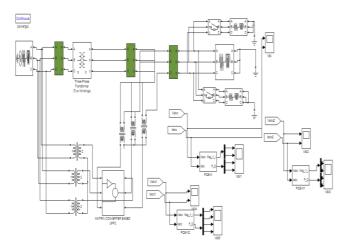


Fig 5: Modeling circuit of the three-phase matrix converter operating as a UPFC with direct power control

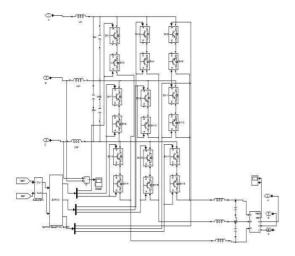


Fig 6: Control scheme of direct power control of the three- phase matrix converter.

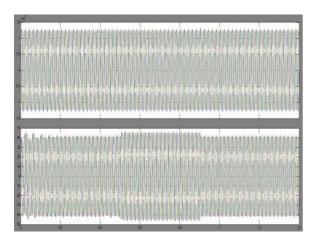


Fig 7: SIMULINK results of proposed UPFC (a) Output voltage, (b) Output current.

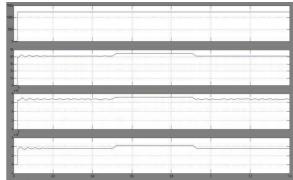


Fig 8: SIMULINK results of proposed UPFC (a) magnitude of Output voltage, (b) Magnitude of output current, (c) Real power, (d) Reactive power

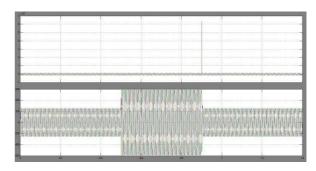


Fig 9: SIMULINK results of without UPFC (a) OutputVoltage, (b) output current.



Fig 10: SIMULINK results of without UPFC (a) Magnitude of output voltage, (b) Magnitude of output current, (c) real power, (d) reactive power

Simulation results confirm the performance of the proposed controllers, showing no cross-coupling, no steady-state error (only switching ripples), and fast response times for different changes of power references. The results (Fig. 7) also show fast response without cross coupling between active and reactive power. This confirms the DPC-MC robustness to input filter parameter variation, the ability to operate at low switching frequencies, and insensitivity to switching nonlinearity.

CONCLUSION

In this Presented simulation results show that active and reactive power flow can be advantageously controlled by using the proposed DPC. Results show no steady-state errors, no cross-coupling, insensitivity to non-modeled dynamics and fast response times, thus confirming the expected performance of the presented nonlinear DPC methodology. The obtained DPC-MC results were compared to PI linear active and reactive power controllers using a modified Venturini high- frequency PWM modulator. Despite showing a suitable dynamic response, the PI performance is inferior when compared to DPC. Furthermore, the PI controllers and modulator take longer times to compute.

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