Simulation of Single Phase Matrix Converter Using SPWM for Low Frequency Motor Control Application

P. Jenitha Deepa
(Professor): Department of Electrical and Electronics Engineering
Saranathan College of Engineering
Tiruchirappalli, India

M. Rizwana1, N. Thaslima2, S. Priyadharshini3
V. Priyadharsini4
(UG Graduate): Department of Electrical and Electronics Engineering
Saranathan College of Engineering
Tiruchirappalli, India

Abstract—This paper presents the concept of Single Phase Matrix Converter topology for the conversion of AC-AC with reduced frequency operation. The main advantage of matrix converter is bi-directional energy flow. The circuit comprises of four ideal switches. The output of this converter is fed into the single phase induction motor. The Sinusoidal Pulse Width Modulation (SPWM) scheme is used to incorporate the output of both open loop and closed loop model for various frequency levels. The topology with required switching strategy is then modelled using MATLAB/SIMULINK.

Keywords—Single Phase Matrix Converter (SPMC), Sinusoidal Pulse Width Modulation (SPWM), Insulated Gate Bipolar Transistor (IGBT).

I. INTRODUCTION

The electrical machine is one that converts electrical energy into mechanical energy. Drive systems are widely used in many applications such as fans, pumps, paper and textile mills, elevators, electric vehicle and subway transportation, home appliances, wind generation systems, servos and the multiple conversion stages and the intermediate robotics, computer peripherals, steel and cement mills, ship propulsion, etc. Among all types of machine, Induction machine is commonly used in industry. Mostly, all industrial applications depends on ac to ac power conversion and the ac to ac converters takes Power from one ac system and delivers it to another ac system with the waveform of different amplitude, frequency or phase.

These converters are becoming popular due to the availability of better switching devices. The Matrix converter was first proposed by Gyugyi in 1976 [1,2].Then it was introduced by Venturini and Alesina in 1979 is the most general converter-type in the family of ac–ac direct converters [3].SPMC can perform 4 type of power conversion AC-AC [4],AC-DC [5],DC-AC [6],DC-DC [7] using the same circuit model. This type of converters has the advantages of bi-directional power flow, sinusoidal input and output waveforms, minimal requirement for reactive energy storage components [8,9]. The ac to ac matrix converters are commonly classified into two categories, one is direct converters and another one is indirect converters. Direct ac–ac matrix converters have a number of advantages when compared to dc link converters. In such converters, power is converted from fixed AC voltage and frequency to variable AC voltage and frequency without any intermediate dc link. The matrix converter has a disadvantage that the maximum output voltage is limited to 86.6% of the input voltage. [10]. The power circuit of a single-phase direct ac–ac converter feeding an induction motor is shown in Fig (1).

![Fig. 1. Block diagram of single phase matrix converter](image)

The circuit performs the frequency and voltage conversion in a single step without using storage elements. Moreover with bi-directional switches, the converter also enables regenerative operation which is a desirable feature in motor control applications [11]. However, dc converters are not able to perform bi-directional power flow without making the input side of the converter controllable [12]. Matrix converter is characterized by sinusoidal waveform that shows the input and output switching frequencies. Generally, simulations were performed to predict the behavior and nature of a circuit. The SPWM block is used to give a pulse to the IGBT of the single phase matrix converter. As compared to conventional drive there is potential for increased power/weight and power/volume ratios. This topology is to simulate the single phase direct matrix converter with reduced frequency. The input is fed from a single phase ac voltage source. The SPWM block is used to give a pulse to the IGBT of the single phase matrix converter. The output of this topology is fed to the
single phase load. Results of the simulation are presented to verify the feasibility of given technique.

II. BIDIRECTIONAL SWITCHES

The IGBT was used for its high switching capabilities and high current carrying capacities which are enviable for high power application. The bidirectional switch is given in Fig (2).

Matrix converter requires the use of bidirectional switches capable of blocking voltage and conduction current in both directions. In the absence of bidirectional switch module, the common emitter anti-parallel IGBT with diode pair can be employed. The diodes provide reverse blocking accomplishment to the switch module [13].

III. SINGLE PHASE MATRIX CONVERTER

The SPMC consists of four ideal bidirectional switches as displayed in Fig (2). In which the source input is coupled with the load. The switches can be denoted as S1, S2, S3, and S4 for the respective four switches where “a” is denoted as forward direction and “b” is denoted as its reverse direction of each switch. The switches s1a, s4a, s2b, s3b conducts for positive half cycle as shown in the Fig (3) and Fig (5). The s1b, s4b, s2a, s3a conducts for negative half cycle as shown in the Fig (4) and Fig (6). Each switch can accomplish conduction current in both directions, blocking forward and reverse voltages. The input and output voltage of SPMC is given by the equation (1) and (2) respectively with loads represented in (3).

\[ V_I(t) = \sqrt{2}V_s \sin \omega t \quad \ldots \ (1) \]
\[ V_O(t) = \sqrt{2}V_s \sin \omega t \quad \ldots \ (2) \]
\[ V_O(t) = R_i I(t) + L \frac{di}{dt} \quad \ldots \ (3) \]

The fundamental component of output voltage is given by the equation (4)

\[ f_o = \frac{f_m - f_i}{2} \quad \ldots \ (4) \]

Where \( f_m \) the modulation frequency and \( f_i \) is the input frequency [14]. The four bidirectional switches S1 to S4 “a” or “b” represent the driver one and two respectively according to the following guidelines:

- S1a and S4a will manage the current flow during positive cycle of the input source shown in Fig (3).
- S1b and S4b will manage the current flow during negative cycle of the input source shown in Fig (4).
- S2b and S3b will manage the current flow during positive cycle of the input source shown in Fig (5).
- S2a and S3a will manage the current flow during negative cycle of the input source shown in Fig (6).

Representation of SPMC with reduced frequency operation requires different bidirectional switching arrangements depending on the desired output frequency [15].

IV. SINUSOIDAL PULSE WIDTH MODULATION

The Sinusoidal Pulse Width Modulation (SPWM) is a well-known technique in power electronics. The switches in the voltage source can be turned on and off as required. Generation of the desired pulses is achieved by comparing the high-frequency triangular ‘carrier’ signal (Vc) with a sinusoidal reference signal (Vref) of the desired frequency is shown in the Fig (7).
Depending on whether the signal voltage is larger or smaller than the carrier waveform, either the positive or negative voltage is applied at the output [16]. The crossover points are used to detect the switching instants. The output voltage of the converter is controlled by SPWM, but the frequency of the converter is changed by controlling the duration of operation of the switch [17]. The value of output voltage is proportional (or) equivalent to modulation index (mi) . The modulation index (mi) is the ratio of the reference signal (Vref) to that of the carrier signal (Vc) [18]. The switching pattern for the required frequency is given in the Table (1).

**TABLE I**

<table>
<thead>
<tr>
<th>Input Frequency</th>
<th>Target Output Frequency</th>
<th>Switch “modulated”</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>25Hz</td>
<td>1 S1a &amp; S4a</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4 S3a &amp; S2a</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3 S2b &amp; S3b</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2 S4b &amp; S1b</td>
</tr>
<tr>
<td></td>
<td>50Hz</td>
<td>1 S1a &amp; S4a</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4 S3a &amp; S2a</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3 S2b &amp; S3b</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2 S4b &amp; S1b</td>
</tr>
<tr>
<td></td>
<td>12.5Hz</td>
<td>1 S1a &amp; S4a</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4 S3a &amp; S2a</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3 S2b &amp; S3b</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2 S4b &amp; S1b</td>
</tr>
</tbody>
</table>

V. SIMULATION

The model is verified through simulation using MATLAB/SIMULINK. The open loop simulation model is displayed in Fig (8) and its corresponding subsystem of SPWM block is shown in the Fig (9). The closed loop simulation model is displayed in Fig (10) and its corresponding subsystem of SPWM block is shown in the Fig (11). The desired output derived from simulation method related to SPMC for different frequencies and the motor for the speed in rpm is shown in figure from Fig(12) to Fig(21).
Fig. 11. Sub Block of Sinusoidal Pulse Width Modulation for closed loop

Fig. 12. Input voltage waveform

Fig. 13. Speed (in rpm) at 25Hz for open loop

Fig. 14. Speed (in rpm) at 25 Hz for closed loop

Fig. 15. Speed (in rpm) at 16.67 Hz for open loop

Fig. 16. Speed (in rpm) at 16.67 Hz for closed loop

Fig. 17. Speed (in rpm) at 12.5 Hz for open loop

Fig. 18. Speed (in rpm) at 12.5 Hz for closed loop

Fig. 19. Output voltage waveform at 25 Hz

Fig. 20. Output voltage waveform at 16.67 Hz

Fig. 21. Output voltage waveform at 12.5 Hz
VI. CONCLUSION

It has been illustrated that the single-phase matrix converter can be simulated and recognized by suitable switching schemes, where IGBTs are used as the main power switching device. When fed from the mains at constant frequency and amplitude, the converter is capable of synthesizing a constant output voltage with variable frequency. From the results obtained, it has been shown that by varying the output frequency the speed of the asynchronous motor can be varied. The output waveform has been synthesized using Sinusoidal Pulse Width Modulation.

VII. REFERENCES