

# Modelling of High Gain DC-DC Converter

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**Abstract**— This paper discusses about modelling of high-gain DC to DC converter with the reduction in voltage stress, which is used in place of regular step up converters. Regular step up converter doesn't stand at high gain due to heavy voltage stress. Here we can use the solar energy, wind energy as a source for the step up converter. Proposed converter works at very high voltage gain compared to the existing topologies such as the twice boost converter and conventional boost converter. The main advantage of the proposed boost converter over the conventional boost converter is that it provides very high voltage gain at lower duty ratio and has better efficiency at higher loads as compared to another similar kind of converters. The converter design and comparison with previous boost converter have been discussed in detail with supporting simulations using MATLAB/SIMULINK.

**Keywords:** High voltage gain, DC-DC converter, duty ratio

## I. INTRODUCTION

In ancient ages, people used to depend upon the Sun only, the most powerful energy supplier on the Earth. Then they used to learn about the use of fire and a positive vibe came to their mind towards the invention of some dependable source of energy. Electricity, being discovered by Benjamin Franklin in the 18th century has opened the eyes of the then Scientists and Researchers for the best future source of energy [1]. The increasing dependency on electricity requires more amount of electricity generation by combustion of fossil fuels by putting a great impact on the green house effect and global warming. Modern technology makes the use of renewable energy sources (RES) to become an alternative of the combustion engines for power generation as the cost and the environmental issues are concerned without any harmful emission [2-3]. Few of these sources include solar, fuel cell, wind energy etc [4-5]. These renewable sources needs power conditioning using power electronic converter [6-8]. Photovoltaic (PV) based system is most popular among various sources of renewable [8-11]. But the output of PV cells are very low so need a isolated/ non isolated DC-DC converter, which step up the low input DC voltage into higher output voltage. Transformer based isolated topologies suffer from limited switching frequency, increased transformer losses, increased voltage stress across the device and are bulky[11-14]. So the non isolated DC-DC boost converters required with reduced switch voltage stress technique for providing a high voltage gain without extreme duty cycle and the use of transformer

link and hence the system cost is reduced [14-17]. Proposed converter works with low voltage stress, high voltage gain, and also provide higher efficiency triple boosting in input voltage.

## II. MODELLING OF DC-DC CONVERTER

One of most important applications of the high gain DC-DC converter is renewable energy generation. The gain of multilevel converter is depends upon the duty cycle of gate pulse. When convectional converter operation is designed into given boost converter operation, only changes the output voltage and voltage gain. The block diagram of the high gain DC-DC Converter is shown in Figure 1. The high gain Boost DC-DC Converter contains only one button (MOSFET), so it has only two operating modes. During start-up mode, the button is turned on and diode D5 is not working. During the second operating mode, the button is in OFF mode and the D5 diode is ON mode. Diode D5 acts as freewheeling condition.

### i. Case-1 (When Switch ON)

During the switch S is in ON condition and diode D1 to D4 are conducting, and D5 works in reversed biased. The switch S is conducting for an interval of DT, where D is the duty ratio and T is the time period of the switching signal. The voltage across the inductors during this mode of operation can be obtained by applying KVL as shown in circuit and the equations are as follows:

$$V_{L1} = -V_{L2} \tag{1}$$

$$V_{L2} = V_{in} \tag{2}$$

$$V_{L3} = V_{in} \tag{3}$$

$$V_{C1} = -V_{C2} = V_{in} \tag{4}$$

Now combine equation no. (1), (2), (3) and (4)

$$V_{L1} = V_{L2} = V_{L3} = V_{C1} = -V_{C2} = V_{in} \tag{5}$$

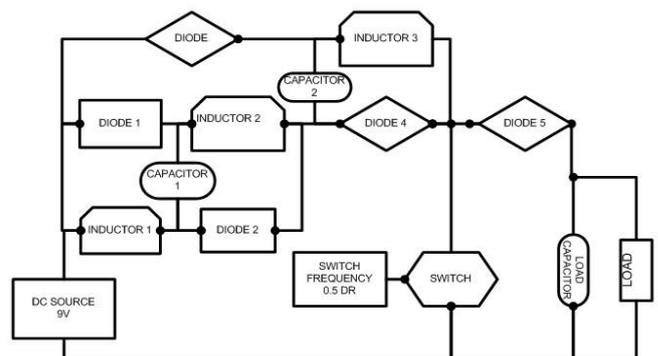


Fig 1 block diagram of proposed high gain converter.

### ii. Case 2 (When Switch OFF)

During the second Condition the switch is in OFF condition, diode D1 to D4 are in reverse biased and D5 is conducting. The voltage across inductor L1, L2, and L3 are the same. If their inductance values are the same as they are in series and the same current is following through them. The voltage across the inductors during this condition of operation can be obtained by applying KVL .

### III. SIMULATION RESULTS

Proposed converter simulate with the help of MATLAB/Simulink software. Then find the output voltage (53.08V), voltage across inductor L3 and current at output capacitor C3 graph in fig 2 and 3.

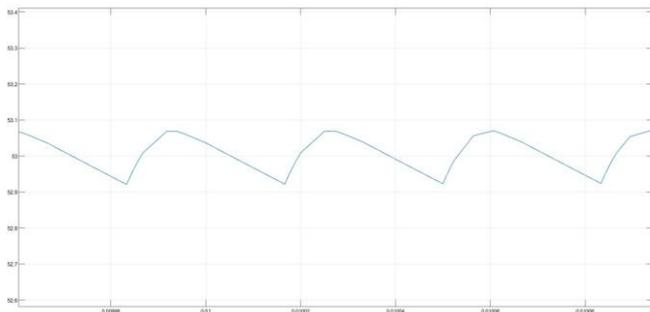


Fig 2.Output waveform of converter during 0.50 duty ratio

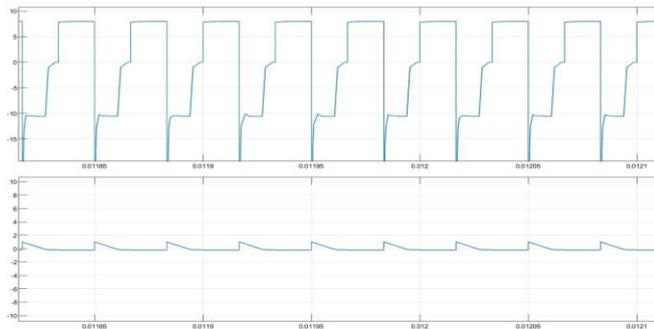


Fig 3.Output waveform input voltage VL3, IC3 during 0.50 duty ratio

### IV. COMPARISON WITH OTHER CONVERTERS

Proposed converter is compared with the other high output voltage gain converters discussing as

1. According to given two tier high gain converter the given output voltage 33.5 V is lower then the proposed model at 0.5 lower duty cycle. There are using two switches for boost output voltage which increases voltage stress and decrease overall efficiency.

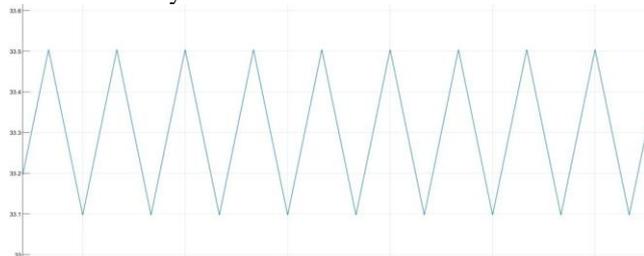


Fig 4.Output waveform of converter during 0.50 duty ratio

2. This double boost high gain converter the given output voltage 24.54V is lower then the proposed model at 0.5 duty cycle. There are also using two switches for boost output voltage which increases voltage stress. So the overall efficiency is also lower then proposed triple boost converter.

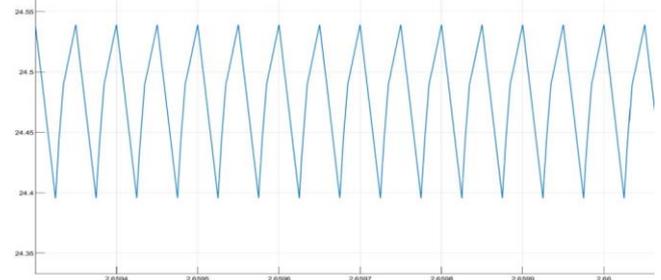


Fig 5.Output waveform of double boost converter during 0.50 duty ratio

### V. CONCLUSION

In this paper you are tripling the DC-DC power converter proposed. The proposed converter is built from a single switch with a 0.5 duty ratio. The output voltage in the case of a proposed DC-DC converter is higher in terms of low performance compared to other parameters presented in Figure 2. For most converters, the loss is much higher in duty ratios, so similarly the output voltage, the proposed converter will work preferably as it will provide the same benefit as the minimum duty rate. Key advantages such as (a) high power gain (b) Smooth and continuous (non-ripple) drawing current from input (c) Flexible structure that provides the required maximum power gain while extracting smooth energy from the source. These key features prove that the proposed high profit converter is the ideal way to integrate a PV input source into standard grid, electric vehicles and other applications.

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