ECCG Acquisition System

Dr. Ganesh V Bhat
Professor
Dept. Of Electronics and Communication Engineering
Canara Engineering College, Mangalore, Karnataka, India.

Bhimasen G Tasaganva
M.Tech student
Dept. Of Electronics and Communication Engineering
Canara Engineering College, Mangalore, Karnataka, India.

Abstract-- Biosignals are recorded as potentials, voltages, and electrical field strengths generated by nerves and muscles. The measurements involve voltages at very low levels, typically ranging between 1μV and 100mV, with high source impedances. Bio signals need to be amplified to make them compatible with devices such as displays, recorders, or A/D converters for computerized equipment. Amplifiers adequate to measure these signals have to satisfy very specific requirements. They have to provide amplification selective to the physiological signal, reject noise and interference signals. This paper, deals with some of the prominent aspects involved in the design of Electrocardiogram (ECG) acquisition system has been discussed with furthermore design and implementation of instrumentation amplifier with appropriate filtering circuits are discussed. The design circuit is tested in real time and results obtained from the test and analysis of result are presented.

Keywords: 3-Lead Electrodes; ECG; instrumentation amplifiers; filters; sallen key topology; differential amplifiers.

I. INTRODUCTION

The ECG means Electro = electrical, Cardio = heart and Gram = recording. Typical lead III ECG with its P, QRS, T and U segments is illustrated in fig.1. Three electrodes, two of them are picking up the biological signal and the third providing the reference potential. The input signal to the amplifier consists of five components:

1. The desired biopotential.
2. Undesired biopotentials.
3. A power line interference signal of 50 Hz and its harmonics.
4. Interference signals generated by the tissue/electrode interface.
5. Noise.

The main challenge in ECG acquisition system is to detect the desired potential and attenuation of the amidst undesired biopotentials, noise and 50Hz electrical interference [2].

The ECG acquisition system consisting of instrumentation amplifier amplifies the potentials and which is used to reject the common mode signals collected from electrodes with the gain of 364. This is followed by a Low pass filter (LPF), High pass filter (HPF) with the cut-off frequency of 220Hz and 0.16Hz respectively to attenuate the noise amidst from electrodes. Followed by the notch filter is used for attenuation of 50Hz line interference.

II. DESIGN AND IMPLEMENTATION

A. Instrumentation amplifier (IA) and right leg driver (RLD).

Proper design of the amplifier provides rejection of large portion of the signal interferences. Main task of IA is to reject the line frequency interference that is electrostatically or magnetically coupled into the body. The desired biopotential appears as a voltage between the two inputs terminals of IA. Rejection of common mode signals is one of the most important characteristics of the IA [1].

AD620 is a low cost, and high accuracy IA having low noise, practically infinite input impedance used in ECG acquisition system that requires only one external resistor to set gain of 1 to 10,000[4].

The common mode voltage on body is sensed and fed back to right leg. The right leg driver circuit as shown in figure2 drives very small amount of current to right leg to equal the displacement current flowing in the body. This circuit also helps to patient’s safety.

![Fig. 1. Normal rhythm of ECG.](image1)

![Fig. 2. IA and RLD circuit.](image2)
- Gain design:
  \[ G = 1 + \left( \frac{49.4K}{R_G} \right) = 1 + \left( \frac{49.4K}{136\Omega} \right) = 364dB. \]

- Analog analysis:

![Analog analysis of IA and RLD.](image)

![Analog analysis of IA and RLD.](image)

Fig. 3. Analog analysis of IA and RLD.

B. Filters

Sallen key topology is used for design of filters in ECG acquisition system and has been designed and tested. Filters uses a unity gain voltage amplifier with practically infinite input impedance and zero output impedance to design low pass, high pass and notch filter.

a. Low pass filter

![LPF circuit.](image)

![LPF circuit.](image)

Fig. 4. LPF circuit.

- Cut-off frequency design:
  \[ F_c = \frac{1}{2\pi \sqrt{R_1R_2C_1C_2}} = \frac{1}{2\pi \times 330k \times 2.2n} = 219 \text{ Hz} \]

Where \( R_1 = R_2 = 330k \) and \( C_1 = C_2 = 2.2n. \)

- Frequency response:

![Fig. 5. Frequency response of LPF.](image)

b. High pass filter

![HPF circuit.](image)

![HPF circuit.](image)

Fig. 6. HPF circuit.

- Cut-off frequency design:
  \[ F_c = \frac{1}{2\pi \sqrt{R_1R_2C_1C_2}} = \frac{1}{2\pi \times 990k \times 1\mu} = 0.16 \text{ Hz} \]

Where \( R_1 = R_2 = 990k \) and \( C_1 = C_2 = 1\mu. \)

- Frequency response:

![Fig. 7. Frequency response of HPF.](image)
c. Notch filter

![Notch filter circuit](image)

Fig. 8. Notch filter circuit.

- Cut-off frequency design:
  \[ F_C = \frac{1}{2\pi \sqrt{R_1 R_2 C_1 C_2}} = \frac{1}{2\pi \times 330k \times 10n} = 48.2Hz \]

  Where \( R_1 = R_2 = 330k \) and \( C_1 = C_2 = 10n \).

- Frequency response:

![Frequency response of notch filter](image)

Fig. 9. Frequency response of notch filter.

III. CIRCUIT OUTLINE

The fig. 10 shows entire circuit diagram of the ECG acquisition system. It consists of IA, active HPF, LPF and notch filter. Right arm (RA) and left arm (LA) electrodes have to be connecting J1 connector which is input to the IA. Here we need perfect linearity regardless of signal amplitude and instant output response regardless of the speed of the signal. Due to these two requirements we have used the AD620 (U1) as IA. And it is type of differential amplifier and that is connected with input buffer amplifiers. The differential amplifier amplifies difference between two potentials from electrodes but does not amplify the particular voltage. It has some important characteristics that are its very low DC offset, low drift, low noise, very high Common mode rejection ratio (CMRR) and input impedance and low output impedance. Gain of the IA can be adjusted by external resistances R1 and R2.

We need flat start and stop band frequency response in filter design to remove undesired potential and noise. Due to this reason we have used Butterworth filter which will uses sallen key topology for its design. The HPF, LPF and notch filters are designed with cut-off frequency of 0.16, 220 and 50Hz respectively. Amplification of the output signal is done by changing the value of resistances R11 and R10. LM358 (U2 AND U4) is used in design of filters.

Unfortunately the human body can acts as an antenna which picks up electromagnetic interference especially 50Hz noise from electric power lines. To avoid this reason we need a Right leg driver circuit (LM358-U3) which will reduces common mode interferences. Right leg connected to output of an U3:A. Due to this interface displacement current flow through output resistance instead of body and patient is ungrounded when high voltage appears between patient and ground. J2 and J3 are power supply and output connectors respectively.

![Circuit diagram of Electrocardiogram acquisition system](image)

Fig. 10. Circuit diagram of Electrocardiogram acquisition system.
IV. EXPERIMENTAL RESULTS

Following figures shows the real time results which are tested in Lab. All the filters are used in system are active analog filters. These filters are designed properly to get noise free output as much as possible. Now days, existing systems are designed with digital filters to reduce the noise and which will increases cost. According to that results are shown below. Fig.11 shows experimental setup of ECG acquisition system.

The output of IA is amidst with undesired potential and line interference. This is removed by proper design of filters as shown by amplified signal in fig.12 and fig. 13.

V. ANALYSIS

Following table shows the analysis of ECG acquisition system. Actual output values are taken from real time results, these values are compared with standard values and comparison is satisfied.

- Heart beat calculation:
  R-R interval as per the experimental result is 800ms.
  \[
  \frac{1}{800 \times 10^{-3}} \times 60 = 75 \text{bpm}
  \]

<table>
<thead>
<tr>
<th>WAVE</th>
<th>NORMAL</th>
<th>ACTUAL OUTPUT</th>
</tr>
</thead>
<tbody>
<tr>
<td>P_WAVE</td>
<td>0.12 to 1.20sec</td>
<td>0.11sec</td>
</tr>
<tr>
<td>QRS_WAVE</td>
<td>&lt;=0.12sec</td>
<td>0.10sec</td>
</tr>
<tr>
<td>T_WAVE</td>
<td>Not usually measured</td>
<td>0.218sec</td>
</tr>
</tbody>
</table>

VI. CONCLUSION

The electrocardiogram is a very important clinical diagnostic tool. To measure the potential difference on the body’s surface successfully amidst the common-mode voltage and noise requires very subtle design. By using RLD circuit, a high-pass filter, low-pass filters, notch filter and an instrumentation amplifier, the signal can be detected amidst the noise. Finally, the acquisition of ECG signal is amplified to make them compatible with devices such as displays, recorders, or A/D converters for computerized equipment. Amplifiers adequate to measure this signal may satisfy to specific requirements.

REFERENCES


