Design of Curved Slot and Parasitic Patch Ultra-Wide Bandwidth Antenna for C and X-Band Applications

Mahima.V. B1, Sabapathi. T2
1 PG Scholar, Department of ECE, Mepco Schlenk Engineering College, Sivakasi, India
2 Associate Professor, Department of ECE, Mepco Schlenk Engineering College, Sivakasi, India

Abstract—The demand for wireless wideband communications is rapidly increasing due to the need to support more users and to provide more information at higher data rates. Recently, Ultra-Wideband (UWB) technology has been used in reliable communication for transmitting data within a short range of frequencies. The objective of the project is to design, simulate, fabricate, and analyze a compact size ultra-wideband microstrip patch antenna designed at a frequency of 8.6 GHz. To provide ultra-wide bandwidth, the antenna is designed using eight parasitic patches and a curved patch with a curved slot, and the ground is made partial. With the help of the parasitic patch, wide bandwidth characteristics can be achieved. FR4 (Flame Retardant4) is used as the substrate. The simulation was performed in ANSYS HFSS software. The designed antenna can cover both C-band (4 to 8 GHz) and X-band (8 to 12 GHz) applications.

Index terms — Parasitic patch, Ultra Wide Bandwidth, Return loss, Radiation pattern, Gain, HFSS

I. INTRODUCTION

Ultra-Wideband (UWB) is an ultra-low power technology that transmits a high amount of data over a short distance. The term “wideband” means that the bandwidth significantly exceeds the coherence bandwidth of the channel. It uses no fixed frequency and can transmit data over wide range of frequencies. UWB corresponds to a wide bandwidth of about 500 MHz or more. The fractional bandwidth should be greater than 20 percent of critical frequency (fc). The UWB standard is IEEE 802.15.4a and the data rate is about 1 Gbps. Data transmission using UWB is safe and secure. The main characteristics of UWB are high precision, high transmission speed, and reliability. The UWB is much better when compared to Wi-Fi, Bluetooth, and optical communication due to its reliability, cost efficiency, scalability, and safety. UWB is used in short range wireless applications, target sensor data collection, home automation, automobile detection systems, tracking applications, precision locating, medical ranging, real time location systems[1].

An antenna is an electrical device which transmits and receives signals. It converts electrical power into radio waves, and vice versa. There are many types of antenna; one such is a microstrip patch antenna which consists of a radiating patch(es) on one side and has a ground plane on another side. The patch acts as a resonant cavity (short-circuit at top and bottom, and open-circuit at the sides). If the antenna is excited at a resonant frequency, a strong field will set up inside the cavity and a strong current on the bottom surface of the patch which results in significant radiation. Some advantages of using microstrip patch antenna are it has light weight; ease of installation and low cost [2]. The microstrip patch antenna can be used in low profile applications, space craft applications, etc. In mobile, small, low cost, low profile antennas are used. Atleast one antenna will be present in mobile phones. These antennas will be used for Bluetooth and GPS and also for 4G LTE cellular communications. LTE (Long Term Evolution) is a 4G mobile communication standard helps to download our data at a faster rate. Now-a-days, 5G applications are also available. The parasitic patches can be used to obtain better bandwidth characteristics overcoming the disadvantages of microstrip patch antenna [3]. Complex patches are used to make the radiation most efficient. Elliptical shaped patches can be introduced to make the design simple and this can be optimized to get a wider bandwidth [4].

II. PROPOSED METHOD

The bandwidth is one of the important parameters which are to be considered in antenna design. But, a microstrip patch antenna provides a narrow bandwidth which is a major drawback. To improve the bandwidth of antenna we can,

- Increase the substrate height
- Use low dielectric constant
- Creating slots on the patch
- Complex design patterns

In this project, the bandwidth is increased using partial ground, changing the position of the feed line, using a curved slot on the patch, and using parasitic patches.

A. Equations for designing an antenna

A microstrip patch antenna along with its dimensions is shown in Fig.1.
The steps involved in designing an antenna include five steps. They are shown below.

Step 1: Width Calculation (W):

The width of the microstrip patch antenna is given by,

\[ W = \frac{c}{2f_0} \sqrt{\frac{\varepsilon_{\text{reff}} + 1}{2}} \]

where, \( \varepsilon \) – Dielectric constant of the substrate
\( h \) – Height of the dielectric
\( W \) – Width of the patch

Step 2: Effective dielectric constant (\( \varepsilon_{\text{reff}} \)):

\[ \varepsilon_{\text{reff}} = \frac{\varepsilon + 1}{2} + \frac{\varepsilon - 1}{2} \left( 1 + 12 \frac{h}{W} \right)^{-1} \]

Step 3: Effective length (\( L_{\text{eff}} \)):

\[ L_{\text{eff}} = \frac{c}{2f_0 \varepsilon_{\text{reff}}} \]

where, \( c \) – Speed of light (3 x 10^8 m/s)
\( f \) – Resonant frequency

Step 4: Calculation of change in length (\( \Delta L \)):

\[ \Delta L = 0.412h \left( \frac{\varepsilon_{\text{reff}} + 0.3}{\varepsilon_{\text{reff}} - 0.258} \right) \left( \frac{W}{h} + 0.264 \right) \]

Step 5: Length Calculation (L):

\[ L = L_{\text{eff}} - 2 \Delta L \]

B. Modelling and Design

From the design formulae, the length and width of the patch is calculated and further the antenna is designed in the software by making some changes in the ground plane, feed line and also the shape of the patch and the slot in order to obtain increased bandwidth characteristics. Table.1 describes the measurements for the designed antenna.

<table>
<thead>
<tr>
<th>Substrate Material</th>
<th>FR4 (Flame Retardant 4)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Substrate Height</td>
<td>1.6mm</td>
</tr>
<tr>
<td>Substrate Dimension</td>
<td>55 mm x 50 mm x 1.6mm</td>
</tr>
<tr>
<td>Length of the feed line</td>
<td>20mm</td>
</tr>
<tr>
<td>Width of the feed line</td>
<td>2.5mm</td>
</tr>
<tr>
<td>Dielectric constant</td>
<td>4.4</td>
</tr>
<tr>
<td>Operating frequency</td>
<td>8.6GHz</td>
</tr>
<tr>
<td>Length of patch</td>
<td>7.63mm</td>
</tr>
<tr>
<td>Width of patch</td>
<td>10.62mm</td>
</tr>
</tbody>
</table>

In this project, three types of antenna design are introduced to obtain wide-band. The schematic model design of the top view (a) and bottom view (b) for these three proposed antenna are shown in Fig.2, Fig.3, and Fig.4.

- **Proposed Model I (Curved Patch with curved slot)**

- **Proposed Model II (Parasitic patch with curved slot)**

| Fig.1 Microstrip patch antenna
| Table.1 Antenna measurements
| ![Fig.2 Proposed antenna design Model-1](image) (a)
| ![Fig.2 Proposed antenna design Model-1](image) (b)

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Fig. 3 Proposed antenna design Model-2

- **Proposed Model III (Parasitic patch and curved patch with curved slot)**

Fig. 4 Proposed antenna design Model-3

III. SOFTWARE AND HARDWARE REQUIREMENTS

Generally to design an antenna, we need software. In this project, the antenna is designed and simulated using the HFSS software. After designing the antenna, fabrication process is done. Later on the fabricated antenna is tested by a test system. Here, Vector Network Analyser (VNA) is used for testing the antenna.

ANSYS HFSS (High Frequency Structure Simulator) is simulation software for designing and simulating high-frequency electronic products. The optimization tool available with HFSS is very useful for antenna engineers to optimize the antenna parameters very accurately. For boundary schemes, Radiation and Perfect-E boundaries are widely used for this design. In this project, HFSS is used to design an antenna which should satisfy increased bandwidth suitable for many applications.

The steps involved in designing the antenna and simulation procedure is mentioned in the following flowchart (Fig.5).

![Flowchart](image)

IV. RESULTS AND DISCUSSION

A. Bandwidth Calculation:

The bandwidth can be calculated as the difference between the higher cut-off frequency ($f_H$) and lower cut-off frequency ($f_L$). It is given by,

$$\text{Bandwidth} = f_H - f_L$$
From the Fig. 6, the bandwidth is calculated using, \( f_H = 7.03 \text{ GHz} \) and \( f_L = 1.93 \text{ GHz} \) and critical frequency \( 8.6 \text{ GHz} \). Thus, the bandwidth obtained is 5.1 GHz. The percentage of the bandwidth is obtained as \((5.1/8.6) \times 100\) i.e. 59.3 %. Hence, the fractional bandwidth for the antenna is 59.3 %.

In the Fig.7, we have \( f_H = 9.91 \text{ GHz} \), \( f_L = 3.77 \text{ GHz} \) and \( f_C = 8.6 \text{ GHz} \). Thus, the bandwidth can be calculated as \((9.63 - 3.67 = 6.41 \text{GHz})\) and the percentage of the bandwidth is obtained as \((5.96/8.6) \times 100\) i.e. 71.39 %. Thus, the impedance bandwidth is 71.39 %.

From the Fig.8, \( f_H = 9.92 \text{ GHz} \) and \( f_L = 2.01 \text{ GHz} \) and \( f_C = 8.6 \text{ GHz} \). Thus, the bandwidth can be calculated as \((9.63 - 2.02 = 7.91 \text{GHz})\) and the percentage of the bandwidth is obtained as \((7.61/8.6) \times 100\) i.e. 91.97 %. Hence, the fractional bandwidth for the antenna is achieved as 91.97 %.

The Fig.6, Fig.7, and Fig.8 show the return loss plot for the desired proposed antenna for the Model-1, Model-2, and Model-3 respectively.

B. Radiation Pattern:

The radiation pattern of microstrip patch antenna is broad. It has a low radiated power and bandwidth is narrow. It has lesser directivity. Fig.9 shows the desired radiation pattern for the proposed antenna for Model-1 design. Here, at 2.26 GHz, the radiation pattern is wider when compared to other frequencies. Fig.10 shows the pattern for the Model-2 design where, it is wider at 8.59 GHz. The radiation pattern for Model-3 design shows a wider range at 8.57 GHz and it is shown in Fig.11.

C. VSWR:

VSWR stands for Voltage Standing Wave Ratio. It is related to the reflection coefficient of an antenna, which describes how much power is reflected from the antenna. In terms of VSWR, a good antenna should satisfy the condition \((1 < \text{VSWR} < 2)\). The VSWR for the proposed antenna for Model-1 design is shown in Fig.12, Model-2 design in Fig.13, and Fig.14 shows the VSWR plot for the Model-3 design.
The gain can be defined as the ratio of maximum power received from the given antenna to the maximum power received from the reference antenna. From the gain obtained, it is clear that the power radiated by an antenna is concentrated at one direction and minimum at other directions.

The antenna parameters for the three proposed model are obtained as shown in Fig.14, Fig.15 and Fig.16 respectively. For the proposed antenna (Model 3), the bandwidth is found to be 7.91 GHz. It offers a wide impedance bandwidth of 91.97% (covering from 2.01 to 9.92 GHz at VSWR ≤ 2), symmetric radiation pattern, and a high gain of 6.8 dB at 7.4 GHz (C-band) and 4.58 dB at 8.6 GHz (X-band). The Model-2 proposed antenna provides a bandwidth 6.41 GHz and a gain of 5.67 dB at 8.6 GHz.
Table.2 Bandwidth comparison table

<table>
<thead>
<tr>
<th>Model</th>
<th>Bandwidth (F_H - F_L)</th>
<th>Fractional Bandwidth (Bandwidth / F_C) x 100</th>
</tr>
</thead>
<tbody>
<tr>
<td>Proposed Model 1</td>
<td>5.1 GHz</td>
<td>59.3 %</td>
</tr>
<tr>
<td>Proposed Model 2</td>
<td>6.41 GHz</td>
<td>71.39 %</td>
</tr>
<tr>
<td>Proposed Model 3</td>
<td>7.61 GHz</td>
<td>91.97 %</td>
</tr>
</tbody>
</table>

Table 2 shows the bandwidth obtained by the proposed antenna for the three different models.

V. CONCLUSION

The proposed microstrip patch antenna is thus designed by using a curved slot and parasitic patches to obtain wide bandwidth characteristics. Additionally, the position of the feed line is also changed and the ground is made partial. The microstrip patch antenna is designed using ANSYS HFSS software and the simulated results are obtained. The return loss curve was plotted radiation pattern and the antenna parameters were obtained. From the simulated results, the proposed Model-3 antenna provides better bandwidth.
characteristics than other antennas. This antenna offers a wide impedance bandwidth of 91.97% (covering from 2.02 to 9.92 GHz at VSWR≤2), symmetric radiation pattern, and provides a gain of 4.58 dBi at 8.6 GHz. These three proposed antenna are at good assent with other works. Since it covers C-band and partially X-band, it can be used in C-band applications and some X-band applications.

VI. FUTURE SCOPE

For further bandwidth enhancement, the design should be optimized to provide an ultra-wide bandwidth i.e. the return loss plot should be obtained as a horizontal straight line satisfying the ultra-wide range. The microstrip patch antenna can be designed using parasitic patch(es), partial ground, slot(s), and DGS(Defective Ground Structure). Parasitic patch and partial ground can help to improve the bandwidth. So with the help of such design we can get better wide bandwidth characteristics and can cover both C-band (4 to 8 GHz) and X-band (8 to 12 GHz) applications.

VII. REFERENCES


