

A Three Level NPC Inverter for Unified Solar PV and Battery Storage System

S. AnithaLakshmi

PG Scholar, Dept. of electrical and electronics
Oxford engineering college
Trichy, India

M. BanuPriya M.E.,

Assistant professor, Dept. of electrical and electronics
Oxford engineering college
Trichy, India

Abstract—an integration of solar PV and battery storage using a three level npc inverter for grid applications. Effectiveness of the proposed methodology is to balance the ac power produced from the solar panel. MPPT is used to find the maximum power fall on PV cell. The MPPT is used to decide which mode to be enabled depends on the solar power available. Here, direct mode represents the power directly produced by solar pv given to grid application. Indirect mode means two battery unit is used to produce simultaneous power to the inverter in absence of solar energy. Here, to connect battery bipolar connection is used. Both relays are connected to the boost converter, which is responsible to stabilize the dc power generated from PV panel. It is also called as dc-dc converter, to step up the voltage when the voltage is not enough for the grid application voltage. The npc inverter function is to improve the output voltage and current waveform. The npc inverter produces three phase sinusoidal voltage /current with fluctuations at the time, inverter receives the signal from PIC controller. The controller is responsible for correct the ac voltage using multicarrier pulse width modulation

Index Terms— Energy resources, MPPT, multilevel inverter, MPWM, boost converter

I. INTRODUCTION

There are many difficulties in the production of electricity due to demand. In order to increase the production of electricity, renewable energy is used. To handle the renewable energy in efficient manner, two important things have to be considered a) converter and b) inverter.

Multilevel inverters have been widely applied in high-power applications. It has been developed to overcome the shortcomings in solid state switching device ratings so that they can be applied to high-voltage electrical systems. That makes unique power electronics topologies suitable for flexible ac transmission systems (FACTS) and custom power applications. The use of a multilevel converter to control the frequency, voltage output real and reactive power flow at a dc/ac interface provides significant opportunities in the control of distributed power systems.

One kind of multilevel inverter is neutral point clamped inverter. The npc inverter improves the output voltage and also reduces the size of the filter. The multilevel inverter is better than the two level inverters like low harmonic distortion of the ac currents, low switching losses. To control the operation of multilevel inverters, a technique called multilevel pulse width modulation (MPWM) is used. The unique

function of multi voltage source inverters allows operating at high voltages.

Dc voltage deviation occurs due to the unbalanced dc capacitors due to tolerances, inconsistency in the switching device characteristics and three phase operation. To solve the problems many strategies have been introduced to balance the neutral point voltage. MPWM have been introduced .here, proper switching sequence can be chosen based on analysis of the predicted voltage unbalance.

The basic operation of npc inverter is divided in to two cycles. One is positive half cycle and another is negative half cycle. In positive half cycle the six switches work and at negative cycle, the remaining six switches work. So the switching loss is reduced.

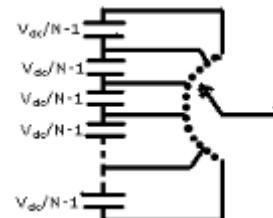


Figure 1 Diagram of NPC inverter

The important concept in this paper is a three level NPC inverter with a combination of these systems on the dc side.

The remainder of the paper is organized as follows section II describes the multilevel pulse width modulation and MPPT algorithm. Section III presents the structure of multilevel inverter. Section IV represents the proposed system and working of controller. Section V describes the simulation result using MATLAB/SIMULINK. Section VI describes the conclusion and prototype set up.

II. CONCEPT OF MPWM AND MPPT

MPWM uses the concept of sub harmonic pulse width modulation, which has the intersection of triangular wave and modulated wave's comparison is made between the two waves, then it generates the output voltage. It requires a carrier much higher frequency than modulation frequency. If the carrier frequency is very high, the result will be a sinusoidal fundamental output with high-frequency harmonics, but minimal low frequency harmonics. For m level inverter m-1carrier with same frequency is considered and also same peak to peak amplitude are disposed continuously.

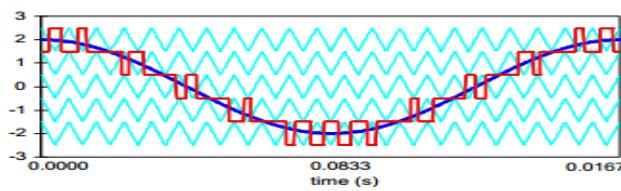


Figure 2 Multilevel carrier based waveform

This method takes average maximum and minimum of three reference voltage to get modulated waveform. The reference is continuously compared with each carrier signal. If the reference is greater than the carrier signal the active device is switched on otherwise the device in off state.

MPPT algorithm is one which includes the charge controllers for extracting maximum available power from PV module under some conditions. The voltage at which PV module can produce maximum power is called peak power voltage. Power varies with solar radiation and temperature. The converter has the capability of MPPT and ac side current control and also having the ability of controlling the battery charging and discharging. This algorithm works effectively in the lack of renewable energy. The process starts by reading the current and voltage which falls on the panel, then it compare the value of each panel from 1 to N-1 cell. If the power is high, it moves to next function. Otherwise the loop back to starting stage.

The Maximum Power Point Tracker (MPPT) is needed to optimize the amount of power obtained from the photovoltaic array to the power supply. The inputs of the MPPT consisted of the photovoltaic voltage and current outputs. The adjusted voltage and current output of the MPPT charges the power supply. A MPPT is used for extracting the maximum power from the solar PV module and transferring that power to the load. A dc/dc converter acts as an interface between the load and the module. By changing the duty cycle the load impedance as seen by the source is varied and matched at the point of the peak power with the source so as to transfer the maximum power.

III STRUCTURE OF MULTILEVEL INVERTER

The three level three phase neutral point clamped inverter is heart of the unified solar PV and battery storage system. The inverter structures consist of twelve switches and six unidirectional switches connected to the midpoint of the source. The capacitor used before the ncp inverter split the neutral voltage to maintain half voltage either positive or negative. The neutral voltage is compensated by MPWM.

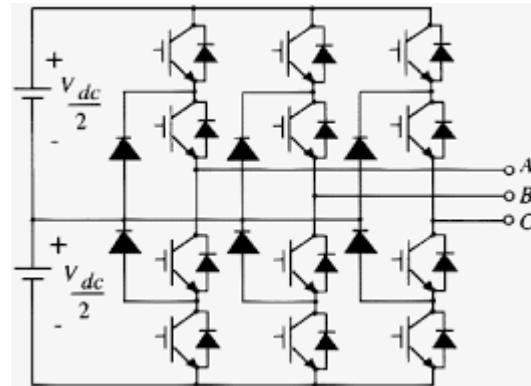


Figure 3 Three level NPC inverter

A NPC inverter includes the DC power source to output DC voltage having a neutral point. An NPC convert DC voltage in to AC voltage in three phases PWM control. When a mode is selected , consider to a first and a second PWM modes by comparing amplitude of voltage reference with a predefined value that is defined by a minimum pulse width, a first voltage reference means to add a predefined bias value at which a changes to positive/negative within a fixed period to voltage references in respective phases in a first PWM mode, a second voltage reference means to fix the voltage reference in one phase by a value with minimum pulse width when voltage reference in one phase is smaller than a described value that is defined by the minimum pulse width in a next PWM mode and correct voltage references of other two phases so as to make line voltage to a value corresponding to the voltage reference, and a modulation frequency varied over to lower PWM control modulation frequency in the first PWM mode and to suppress power loss caused by switching in the first PWM mode.

IV PROPOSED SYSTEM

This paper is concerned with the design of grid-connected three-phase solar PV system integrated with battery storage uses a boost converter and only one three-level inverter. MPPT have the ability to control the battery charging and discharging.

A modulation technique [MPWM] can generate pulse for NPC inverter to perform switching operation. This will provides lower cost, better efficiency and also increased flexibility of power flow control. A Photo Voltaic (PV) system means the conversion of sunlight directly into electricity. The basic device of a PV system is cell. Cells are grouped together to form panels or arrays.

The voltage and current available at the terminals of a PV may directly feed small loads. Many applications require electronic converters for process the electricity from the PV. These converters may be used to regulate the voltage and current at the load and also control the power flow in grid systems. It is mainly used to track the maximum power point (MPP) of the device. In order design a PV systems, one first needs to know how to model the PV device that is attached to the converter.

PV devices present a nonlinear I-V characteristic with several parameters that need to be adjusted from experimental data of practical devices. The mathematical model of the PV device may be useful in the study of the dynamic analysis of

converters, in the study of MPP tracking (MPPT) algorithms, and mainly to simulate the PV system and its components using circuit simulators. The important note is using only a single battery to perform the function as better.

The solar power plant produces the power to the micro controller which gives the control signal to the relay unit. The relay unit decides where the power is stored in the battery either active power or reactive power battery. Then, the inverter converts the direct current into alternating current. The control strategy is applied to the inverter for controlling purposes. MPPT control scheme is applied in the inverter circuit. The final output power is used for the grid application or any other real time application.

PIC controller and decoupling structure, the inverter requested voltage vector calculated. It transfer the specified amount of power to the grid, the battery will be charged using a surplus energy from the PV or will be discharged to support the requested power. The switching operation of the inverter depends on the MPWM (multi level pulse width modulation).the appropriate selection of vector depends on the timing calculation.

The MPPT block determines the requested dc voltage across the PV to achieve the MPPT condition. This voltage can be determined by using another control loop, with slower dynamics using the measurement of the available PV power. After evaluating the requested voltage, the appropriate waves in the can be determined .the selection of the carrier signal will determine which capacitor is to be charged or discharged.

Based on the control system diagram on the ac side, the requested power is either direct or indirect mode. It is applicable at the time excess power generation by source. The system function depends on the integration solar panel with a NPC inverter through the single battery and a boost converter for grid application using a multilevel pulse width modulation. The converter has the capability to perform MPPT function. The capacitor used behind inverter is assumed to be constant. The capacitor in the circuit divides the dc source to operate the in direct and indirect mode.

The direct mode represents the flow of power directly from source and also the indirect mode specifies the power generated by PWM unit to achieve uninterrupted supply.

The block diagram represents the function of the whole circuit which is mainly used for stabilize the ac voltage wave without any ripples.

Here, two batteries are used for storing the excess energy generated by the solar panel. A bidirectional link is used for storing and supplying the power. When the null energy is in battery1 at the time battery 2 is enabled. otherwise it simply stores the energy. These function decided by the PIC controller.

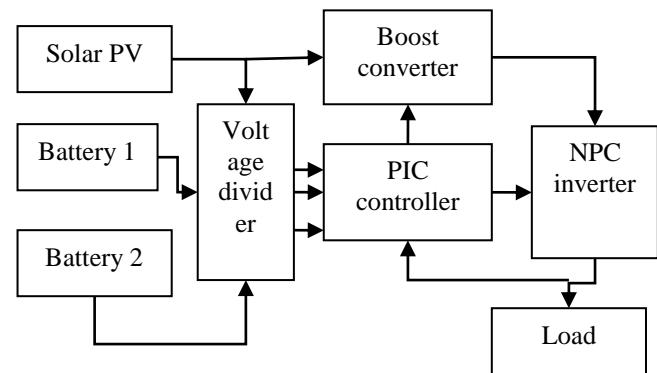


Figure 4 Block diagram of the circuit

V SIMULATION RESULT

The simulation results are examined using a software MATLAB/SIMULINK. The MATLAB is a high performance language for technical computing integrates computation, visualization and programming in an easy-to-use environment where problems and solutions are expressed in familiar mathematical notation. MATLAB serves as a best tool for signal analysis. Open the Simulink library browser. You need MATLAB running before you can open the Simulink browser. Start MATLAB, and then in the matlab command window, enter Simulink the Simulink library opens. From the Simulink library browser menu, select file> new > model.

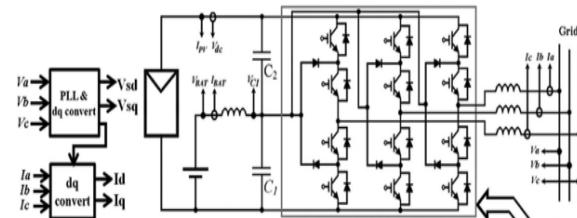


Figure 5 circuit diagram

Simulation steps

- (1) Generation of solar irradiance
- (2) Boost converter output
- (3) NPC inverter step output for single phase
- (4) Three phase signal (correct ac)
- (5) Utility grid current and voltage

A. Generation solar irradiance

The light and heat energy falls on the PV panel, the waveform shows the irradiance. The irradiance may be varied depends on the weather conditions in real time. In the circuit we use current and voltage sensor to measure power. The maximum power is tracked until finding requested power continuous. Otherwise the reference value is given to the PID controller. The energy is stored in an energy storage unit. After the battery unit, the circuit has the boost converter, inverter and utility grid section. A separate unit is placed above the block to reduce total harmonic distortion occur in ac.

For the simulation the input value of irradiance is 1000KW/m².

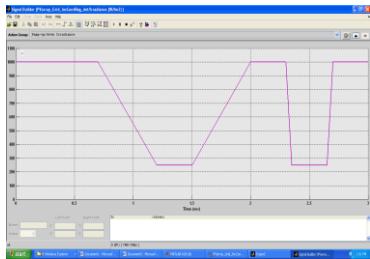


Figure 6 input waveform

B. Boost converter output

In the case limited amount of power generation, it determines how much voltage to be step up for the grid application.

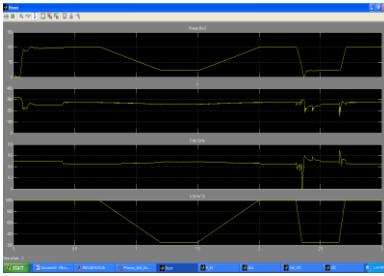


Figure 7 dc to dc converter output

C. NPC inverter output

The direct current is transformed into alternating current using NPC inverter without switching loss. At the midpoint of capacitor always generate zero voltage. So it does not affect the voltage.

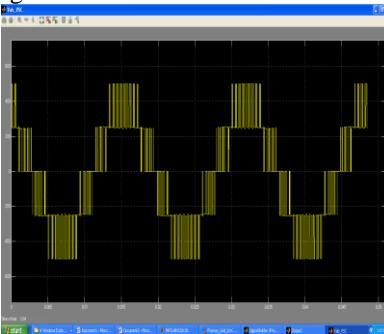


Figure 8 step increased for sine wave

D. Three phase generation

After applying the dq transformation, the three phase voltage is controlled to generate correct ac without fluctuations. Here the function is performed by the phase locked loop, which is used to achieve the output as similar to input.

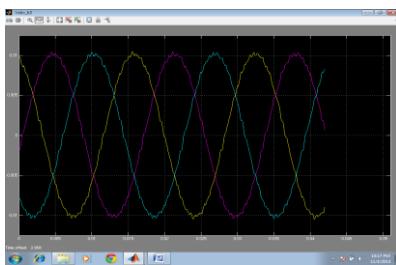


Figure 9 assumption for three phase

E. Utility grid current and voltage

The output waveform of grid application, it can get the power supply without any loss. Here, the utility grid is the application used in simulation.

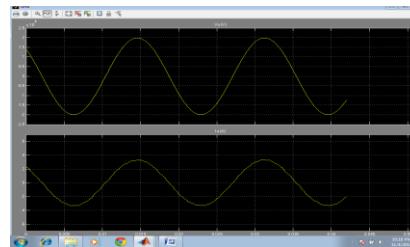


Figure 10 grid output

VI HARDWARE DESCRIPTION

A. SOLAR PANEL

The specification of the solar panel for this paper has the maximum power of 3W.

B. Energy storage unit

The rechargeable batteries used for this paper is 6V lead acid accumulator, which is used to store the electrical energy by some chemical reactions.

C. Voltage divider

It is used to prevent the pic controller from maximum voltage to 5V, which is acceptable by ADC port.

D. PIC16F778A

Used for compare the actual and reference voltage used for modulating multi level inverter. The system is controlled by the peripheral interface controller for generating the pulse depends on solar and energy storage unit .

E. Boost converter

To step up the received voltage depends the requirement of Application.

VII CONCLUSION

A novel topology for a three-level NPC voltage source inverter that can integrate both renewable energy and battery storage on the DC side of the inverter has been presented. An effective Multilevel pulse width modulation (MPWM) method for multi-level inverter fed induction motor is proposed based on SPWM method for two-level inverter. A theoretical framework of a novel extended unbalance three-level modulation technique that can generate the correct ac voltage under varying dc voltage conditions has been proposed. A new control algorithm for the proposed system has also been presented in order to control power flow between solar PV, battery, and grid system, while MPPT operation for the solar PV is achieved simultaneously. The effectiveness of the proposed topology and control algorithm was tested using simulations and results are presented. The results demonstrate that the proposed system is able to control ac-side current, and battery charging and discharging currents at different levels of solar irradiation. The hardware set up was implemented using the same simulation circuit and the load is regulated. Through the hardware output, unity power factor is achieved.

VIII ACKNOWLEDGMENT

I am very much grateful to our Hon'ble Chairman, Er. M. SUBRAMANIAM, and our Principal Dr. G. SEETHARAMAN, Mr. R.GANESAN, M.Tech,(Ph.D) head of the department, Mr. M.BANUPRIYA, M.E for her valuable guidance, assistance and support throughout my studies and also for all the given valuable information, that might help me for my future career. I express my sincere thanks to my faculty advisor Ms. M.BANUPRIYA, M.E for providing all facilities for the completion of the work and other staff members for their guidance and suggestions, for completing my project work.

REFERENCES

- [1] "Neutral-point voltage balancing method for three-level inverter systems with a time-offset estimation scheme" Ui-min Choi and Kyo-Beum lee2013.
- [2] M. A. Abdullah, A. H. M. Yatim, C. W. Tan, and R. Saidur, "A review of maximum power point tracking algorithms for wind energy systems," Renewable Sustainable Energy Rev., vol. 16, no. 5, pp. 3220–3227, Jun.2012.
- [3] Lewicki, Z. Krzeminski, and H. Abu-Rub, "Space-vector pulse width modulation for three-level npc converter with the neutral point voltage control," IEEE Trans. Ind. Electron., vol. 58, no. 11, pp. 5076–5086, Nov.2011.
- [4] S. Burusteta, J. Pou, S. Ceballos, I. Marino, and J. A. AL Zola, "Capacitor voltage balance limits in a multilevel-converter-based energy storage system," in Proc. 14th Eur.Conf. Power Electron. Appl., Aug./Sep. 2011, pp. 1–9.
- [5] Yazdani, A. R. Di Fazio, H. Ghoddami, M. Russo, M. Kazerani, J. Jatskevich, K. Strunz, S. Leva, and J. A. Martinez, "Modeling guidelines and a benchmark for power system simulation studies of three-phase single-stage photovoltaic systems," IEEE Trans. Power Del., vol. 26, no. 2,pp. 1247–1264, Apr. 2011.
- [6] "An effective space-vector pwm method for multi-level inverter based on two-level inverter" P.Satish Kumar, J.Amarnath and s.v.l.narasimham2010.
- [7] M. Bragard, N. Soltau, S. Thomas, and R. W. De Dockers, "The balance of renewable sources and user demands in grids: Power electronics for modular battery energy storage systems," IEEE Trans. Power Electron., vol. 25, no. 12, pp. 3049–3056, Dec. 2010.
- [8] Z. Jian-Yong, S. Zhang-Liang, M. Jun, and L.-f. Wang, "An improved neutral-point voltage balancing algorithm for the npc three-level inverter based on virtual space vector PWM," in Proc. Int. Conf. Elect. ControlEng., Jun. 2010, pp. 3283–3287.
- [9] J. Rodriguez, S. Bernet, P. K. Steamer, and I. E. Lizama, "A survey on neutral-point-clamped inverters," IEEE Trans. Ind. Electron., vol. 57, no. 7, pp. 2219–2230, Jul. 2010.
- [10] O. M. Toledo, D. O. Filho, and A. S. A. C. Diniz, "Distributed photovoltaic generation and energy storage systems: A review," Renewable Sustainable Energy Rev., vol. 14, no. 1, pp. 506–511, 2010.
- [11] "Closed-loop control of a three-phase neutral-point-clamped inverter using an optimized virtual-vector-based pulse width modulation" sergio busquets-monge, Jose daniel ortega, joseph bordonau, Jose antonio beristáin, andJjoan rocabert.2008.
- [12] Yazdani and P. P. Dash, "A control methodology and characterization of dynamics for a photovoltaic (PV) system interfaced with a distribution network," IEEE Trans. Power Del., vol. 24, no. 3, pp. 1538–1551, Jul.2009.
- [13] "Comprehensive approach to modeling and simulation of photovoltaic arrays" Marcelo Gradelha Villalva, Jonas Rafael Gazoli, and Ernesto Ruppert filho.2009.
- [14] "Modeling and circuit-based simulation of photovoltaic arrays" Marcelo Gradelha Villalva, Jonas Rafael Gazoli, Ernesto Ruppert filho2009.
- [15] "For three-level npc - investigation of dc link imbalance using fpga ip core" C.bharatiraja R.lathe dr.s.jeevananthan, s.raghv dr.s.s.dash.2007.
- [16] J. Pou, D. Boroyevich, and R. Pindado, "New feed forward space-vector PWM method to obtain balanced ac output voltages in a three-level neutral-point-clamped converter," IEEE Trans. Ind. Electron., vol. 49, no. 5,pp. 1026–1034, Oct. 2002
- [17] "A fast space-vector modulation algorithm for multilevel three-phase converters," IEEE Trans. Ind. Applicant., vol. 37, no. 2, pp.637–641, Mar./Apr. 2001.
- [18] N. Celanovic and D. Boroyevich, "A comprehensive study of neutral point voltage balancing problem in three-level neutral-point-clamped voltage source PWM inverters," IEEE Trans. Power Electron., vol. 15,no. 2, pp. 242–249, Mar. 2000.
- [19] C. Newton and M. Sumner, "Neutral point control for multi-level inverters: Theory, design, and operational limitations," in Proc. IEEE Industry Applications Soc. Annu. Meeting, vol. 2, New Orleans, LA, Oct.5–9, 1997, pp. 1336–1343.
- [20] D. H. Lee, S. R. Lee, and F. C. Lee, "An analysis of midpoint balance for the neutral-point-clamped three-level VSI," in Proc. IEEE Industry Applications Soc. Annu. Meeting, vol. 2, Toronto, ON, Canada, Oct. 2–8,1993, pp. 965–970.